



Attorney's Docket No.: 07977/204002/US3480D1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : H. Ohtani  
Serial No.: 09/389,393  
Filed : September 3, 1999  
Title : SEMICONDUCTOR DEVICE HAVING INSULATED GATE ELECTRODE

Art Unit: 2815  
Examiner: Baumeister, B.  
Confirmation No.: 5375

Commissioner for Patents  
Washington, D.C. 20231

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PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

In the claims:

Please cancel claims 6-8, 11-13, 15-18, 20-23, 33-37 and 39-43 as follows.

Pease amend claim 1 as follows.

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1. [Amended] A semiconductor device comprising:

G1 a semiconductor region comprising a source region, a drain region, a channel region, and a pair of regions between the channel region and the source and drain regions, said pair of regions formed of a same material as said channel region wherein

03/27/2003 AWONDAF1 00000048 061050 09389393

01 FC:1252 410.00 OP  
02 FC:1201 84.00 OP  
03 FC:1202 432.00 OP

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

March 13, 2003  
Date of Deposit  
Signature *R. P. Ippolito*

Roxanne Ippolito  
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